

**SWARNANDHRA COLLEGE OF ENGINEERING AND TECHNOLOGY**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**(Accredited by NBA)**  
**BOARD OF STUDIES-MINUTES OF MEETING**

**Date:28.10.21**

The Board of Studies meeting of the ECE department was held on 28<sup>th</sup> October 2021 at 4.00P.M through online for the approval of R19, V -VIII Semester Syllabus and R20, III and IV Semester Syllabus

The following members attended the BOS meeting

S.NO.	Name of BOS Member	Designation in BOS
1.	Dr.B.SRao	Chairman
2.	Dr.B.T.Krishna	University Nominee
3.	Dr.N.N.Murthy	Subject Expert Nominated by Institute
4.	Dr.K.Sri Rama Krishna	Subject Expert Nominated by Institute
5.	Mr.S.Naveensrisai	Alumni Member
6.	Dr.B.Sada Siva Rao	Internal Member
7.	Dr.K.Balamurugan	Internal Member
8.	Dr.N.Kopperundevi	Internal Member
9.	Mr.J.E.N. Abhilash	Internal Member
10.	Mr. D. Sekhar	Internal Member
11.	Mr.K.V.B.Chandra Sekhara Rao	Internal Member
12.	Mr.T.J.Alexander	Internal Member

The BOS Chairman Dr.B.SubrahmanyeswaraRao welcomed all the Experts and Members.He starte the presentation with agenda for the approval of R19- (V Semester-VIII Semester) Syllabus and R20-( III and IV Semester) Syllabus

After the presentation of the BOS Chairman, the room is given to the experts for discussion.





**Dr. B. T. Krishna, HOD, ECE Dept., UCEK, JNTUK, Kakinada, University Nominee,**

1. Suggested to add Voltage Regulators topic in Linear & Digital Integrated Circuits Applications instead of Electronics Circuits II.
2. Suggested to revise the syllabus Analog Communication by reducing few topics.
3. Suggested to revise the topics overlapping from Antenna Wave Propagation and Microwave and Optical Communication.
4. Suggested to do DSP Lab experiments by using advanced DSP Kits like TMS6712.
5. Suggested to use more sophisticated software applications for lab experiments.
6. Advised to add one more textbook for Transform Techniques (Elective I).

**Dr. N. N. Murthy, Assoc. Prof., EEE Dept, IIT, Tirupathi, Subject Expert,**

1. Suggested to prescribe two text books for each course and to mention unitwise coverage.
2. Suggested title for VLSI System Design as VLSI Design for both theory and lab.
3. Suggested to change title of Processors and Controllers.
4. Suggested to include Gate Syllabus in all possible courses.
5. Advised to check the overlapping topics in CVRV and Analog Communications.

**Dr. K. Sri Rama Krishna, Principal, PottiSriramuluChalavadiMallikarjuna College of Engineering & Technology, Vijayawada, Subject Expert,**

1. Suggested to add few FET Based model in Electronics & Circuits II.
2. Suggested to shift the topic of scaling from Unit III to Unit II in VLSI Design Course
3. Suggested to conduct individual project based experiments to encourage students in developing their practical skills.
4. Suggested to prescribe one or two text books for each Course and to mention unitwise coverage.
5. Suggested to mention different E sources for every Courses in the syllabus after the reference text books
6. Academic gaps to be identified for every course and to be fulfilled by arranging Guest Lectures, Seminars etc.

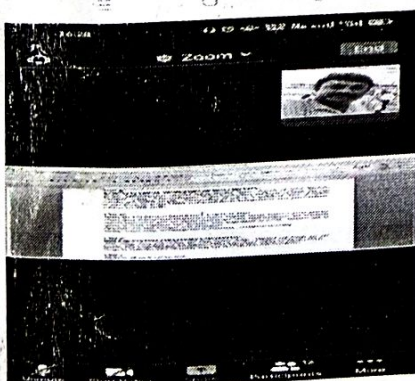
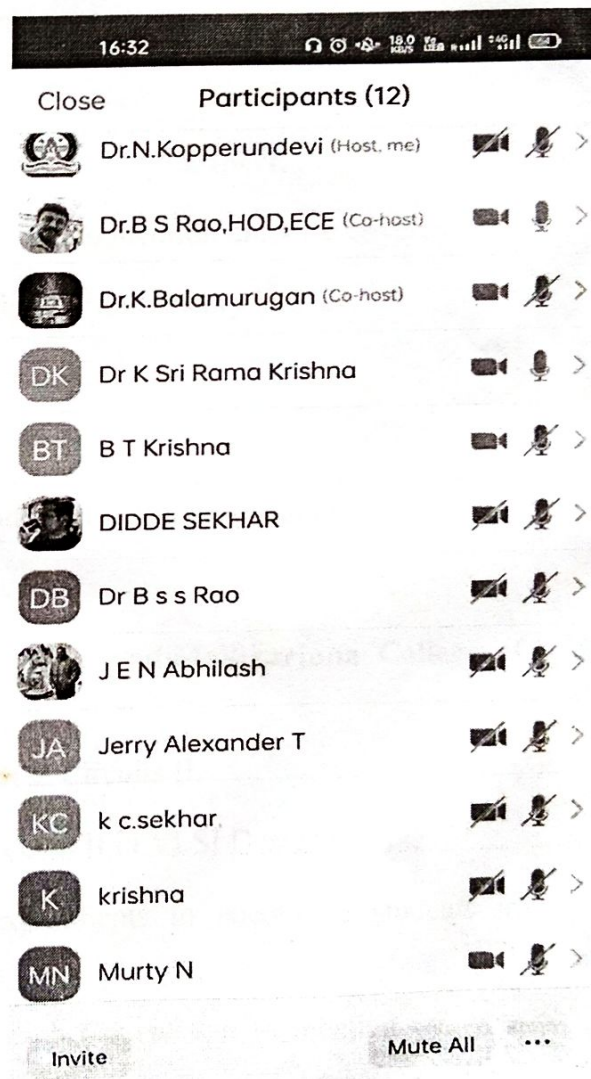
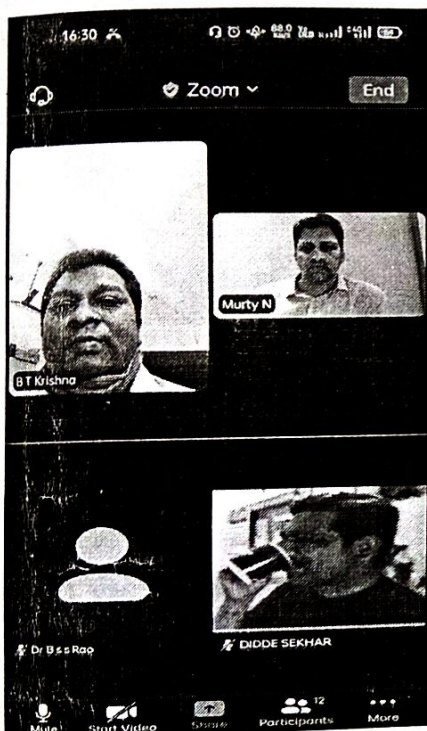




## General

1. All the experts extended their satisfaction on the syllabus of R19- (V Semester-VIII Semester) Syllabus and R20-( III and IV Semester).
2. Appreciated the selection of Elective Subjects .

The BOS Chairman concluded the session and informed that the suggested points will be implemented and mail the same for approval. He requested the Experts to approve the corrected syllabus of R19-V Semester-VIII Semester and R20-III and IV Semesters and ended with Vote of Thanks.



*B.S.Rao*  
**Dr.B.S.Rao**  
**BoS Chairman**  
**Department of ECE**